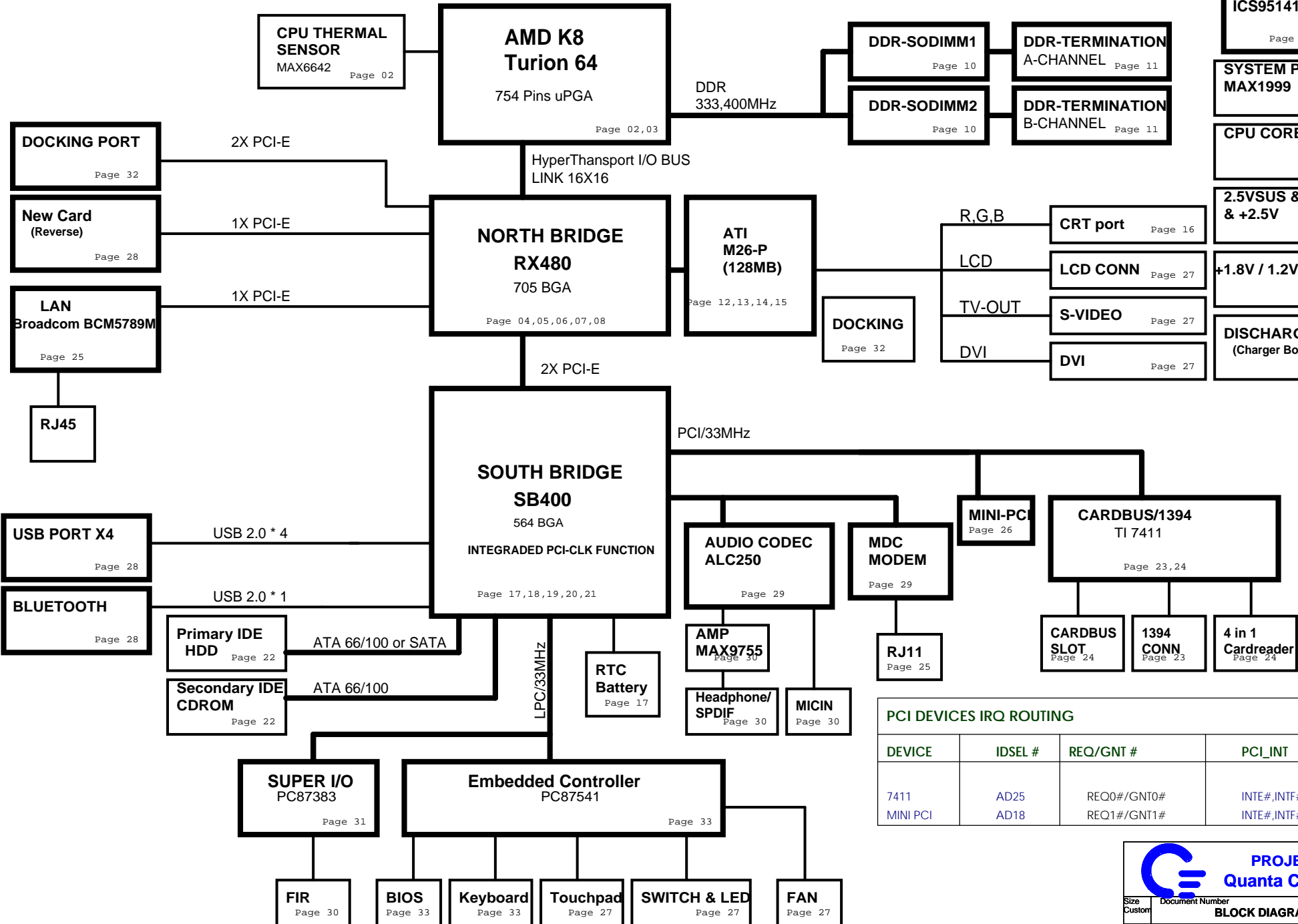
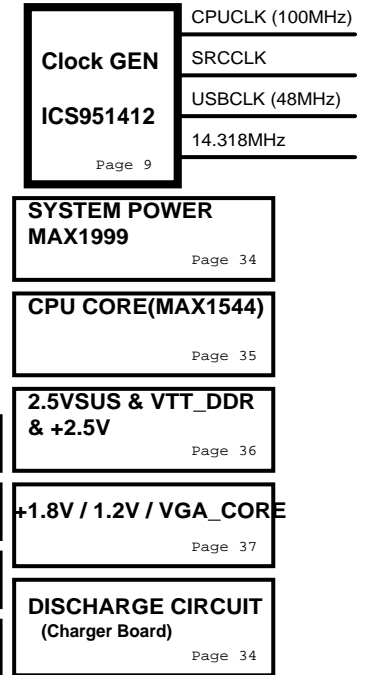


ZF3 BLOCK DIAGRAM

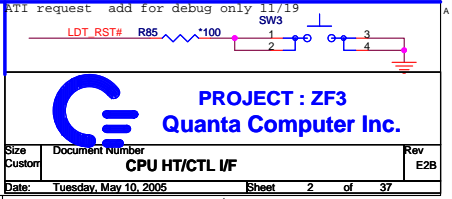
AMD K8/RX480/SB400

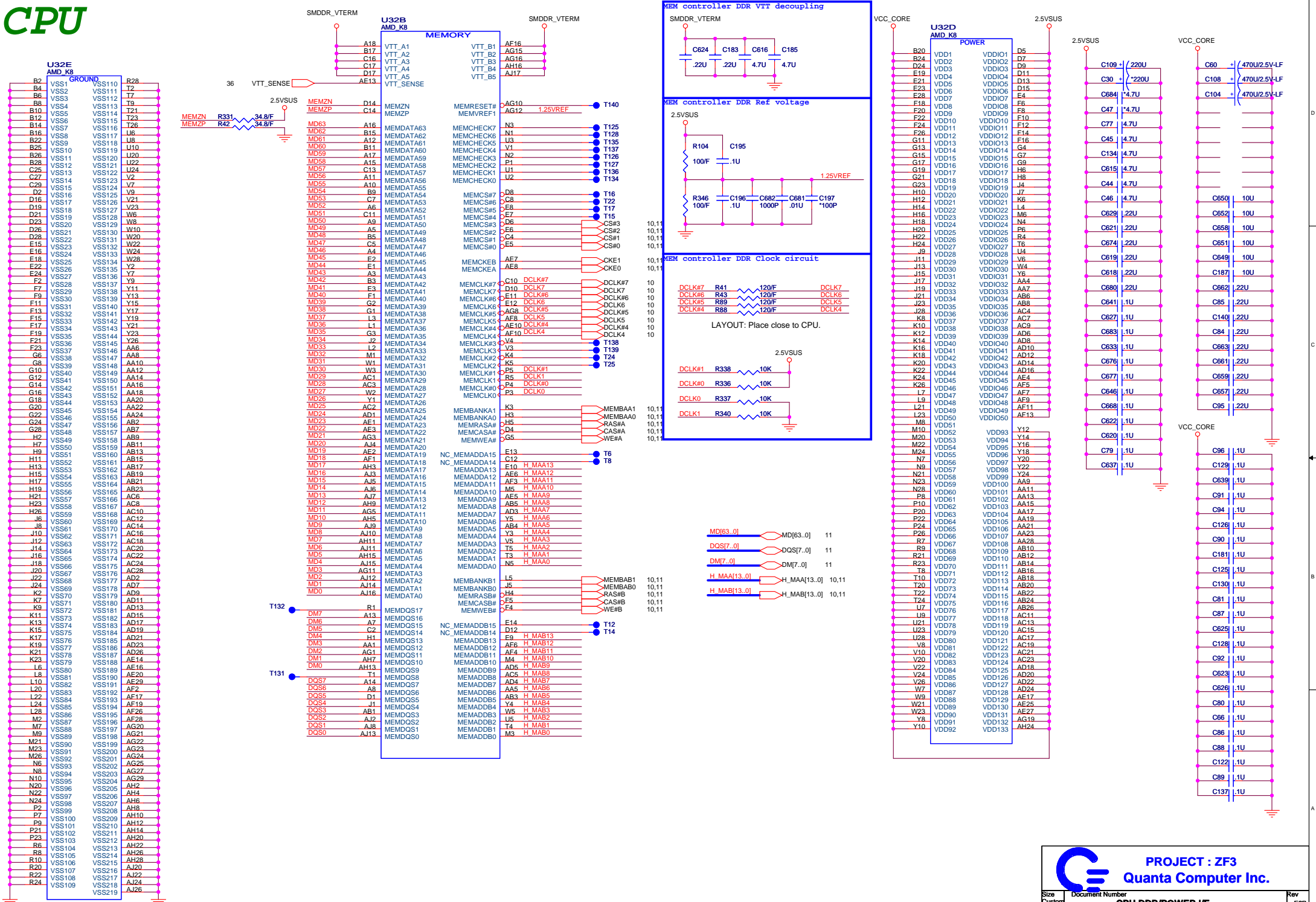
REV: G3A



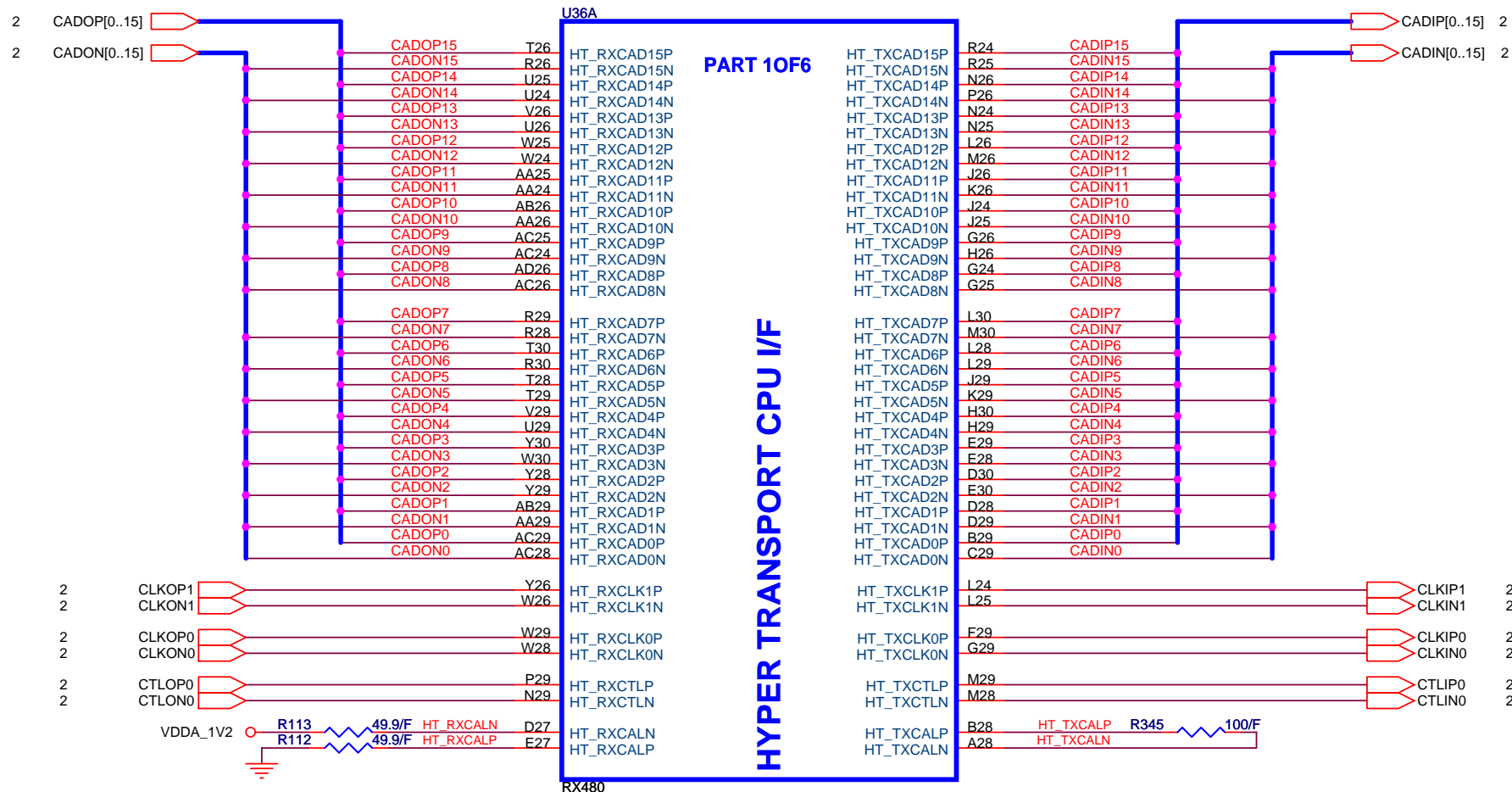
PCI DEVICES IRQ ROUTING			
DEVICE	IDSEL #	REQ/GNT #	PCI_INT
7411	AD25	REQ0#/GNT0#	INTE#,INTF#,INTH#
MINI PCI	AD18	REQ1#/GNT1#	INTE#,INTF#

WWW.AliSaler.Com





NB

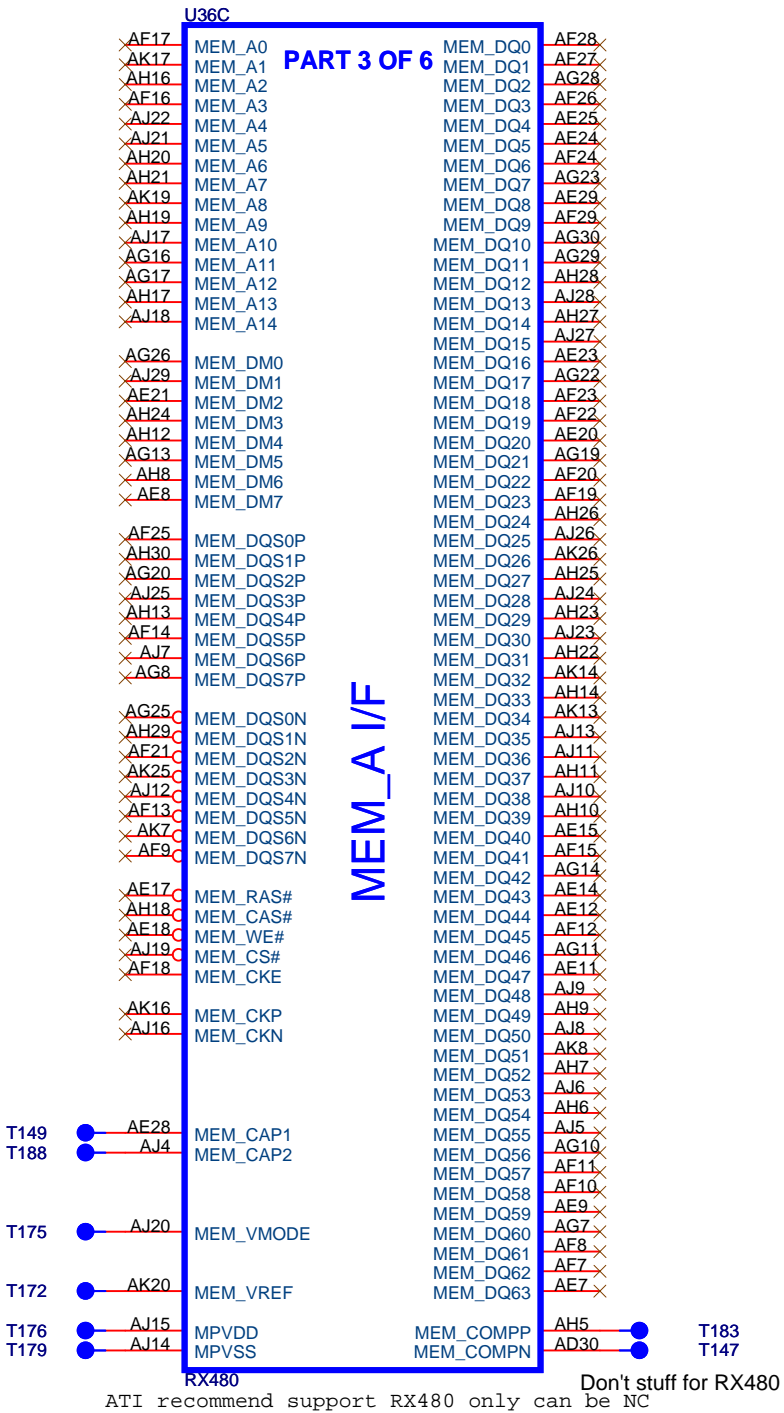


PROJECT : ZF3
Quanta Computer Inc.

Size: Custom Document Number: **RS480M HT LINK I/F** Rev: E2B

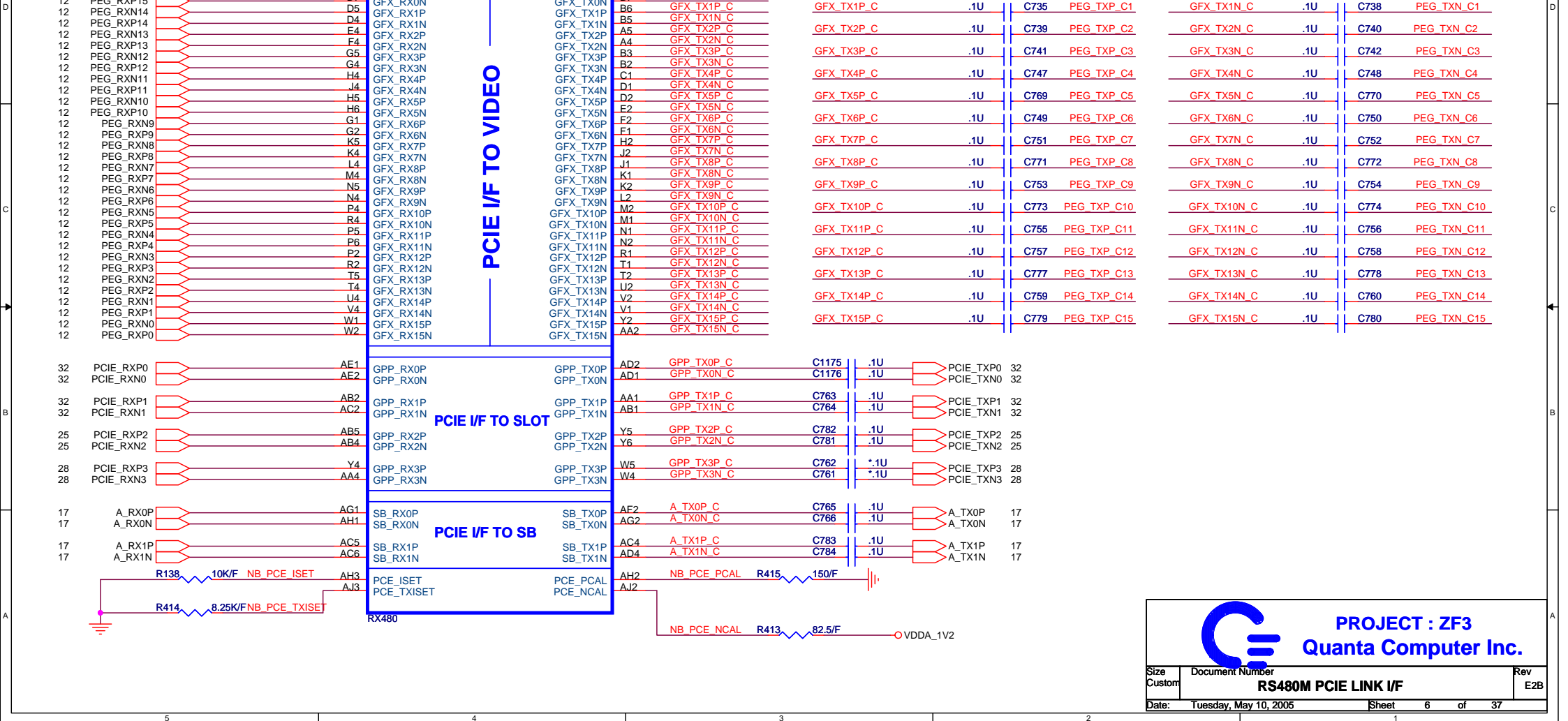
Date: Tuesday, May 10, 2005 Sheet: 4 of 37

NB



PROJECT : ZF3
Quanta Computer Inc.

Size Custom	Document Number RS480M SIDE-PORT MEM I/F	Rev E2B
Date: Tuesday, May 10, 2005	Sheet 5 of 37	



NB

D

C

B

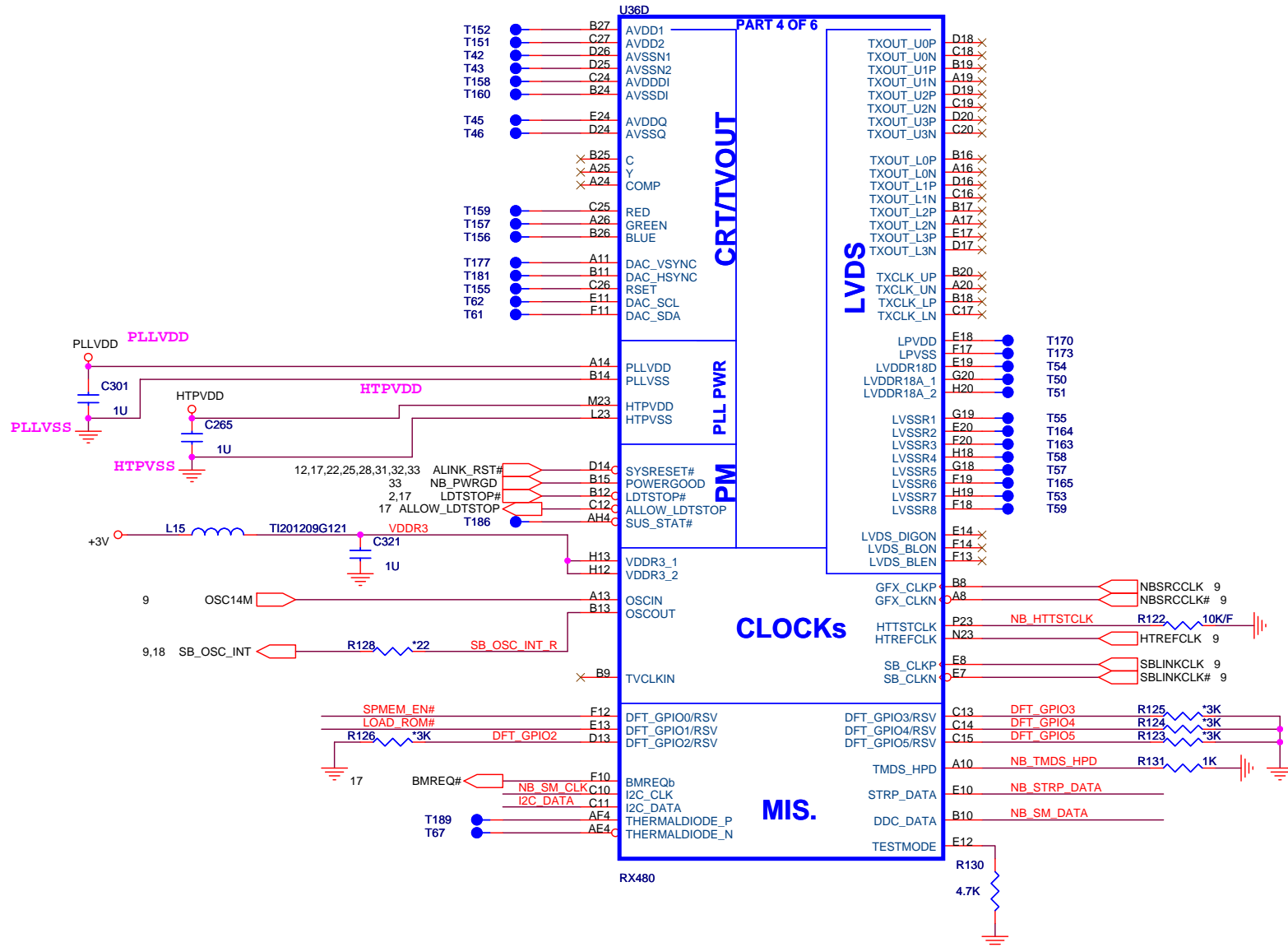
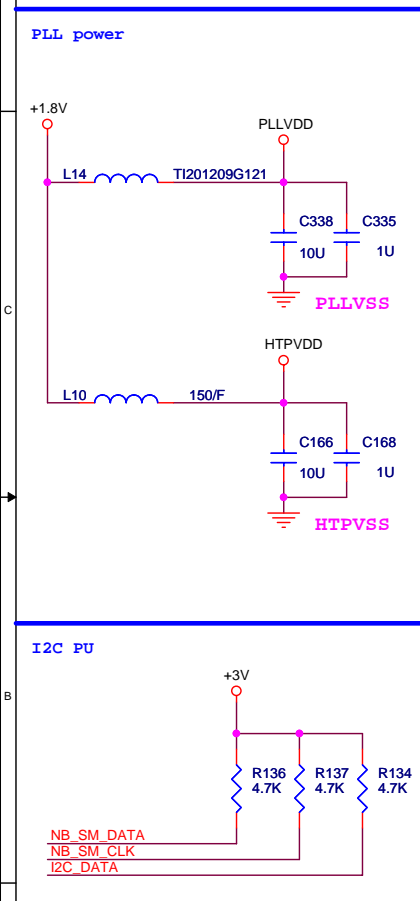
A

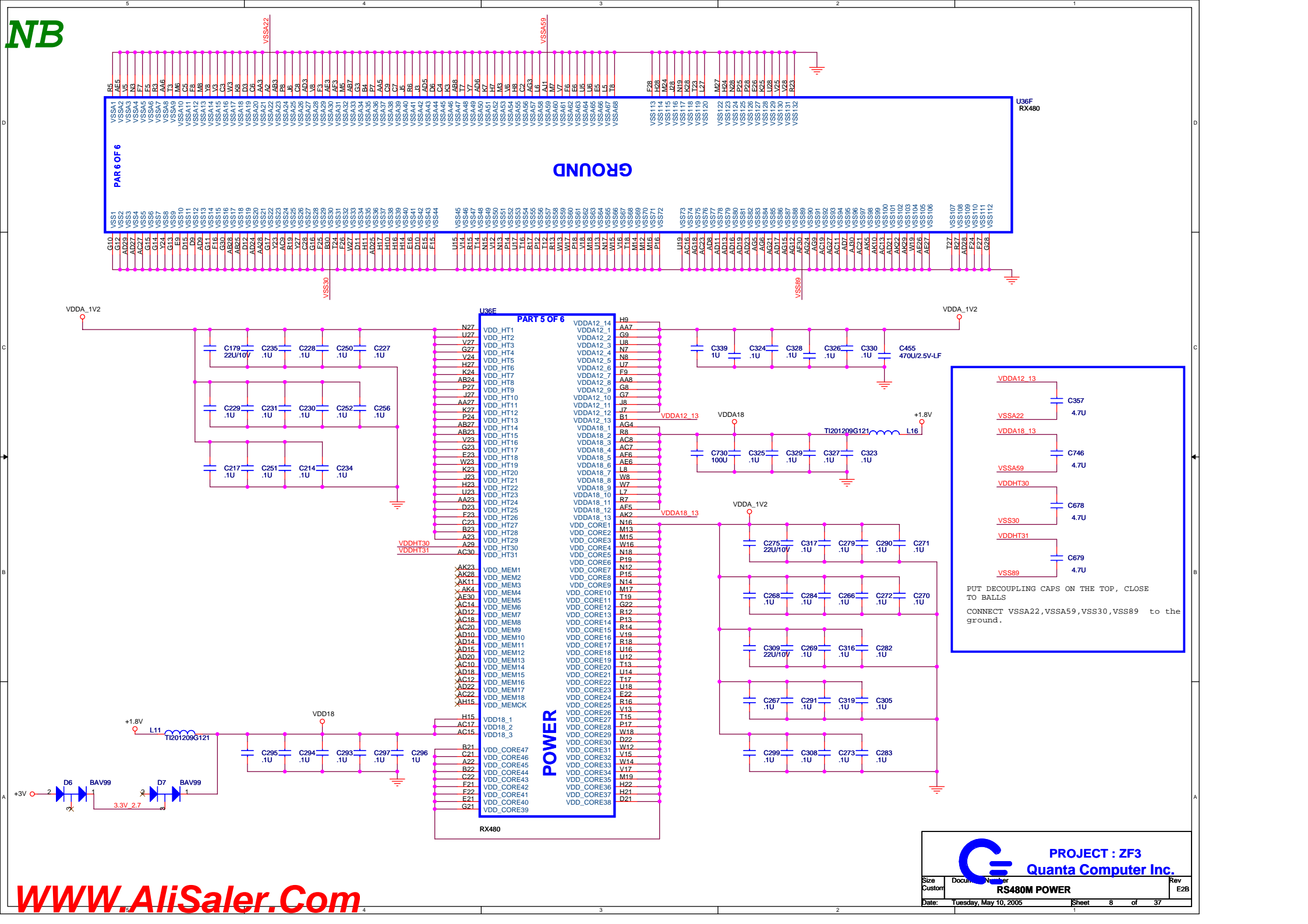
D

C

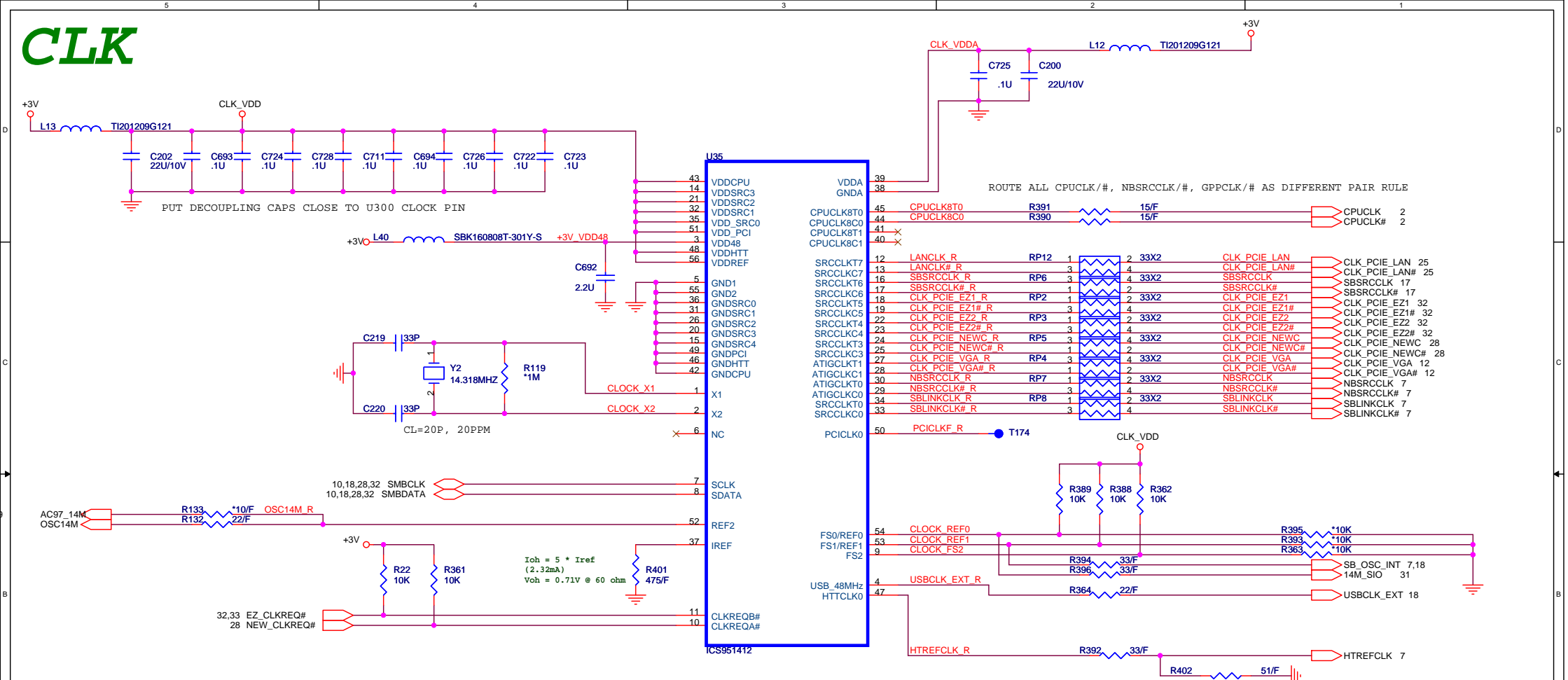
B

A





CLK

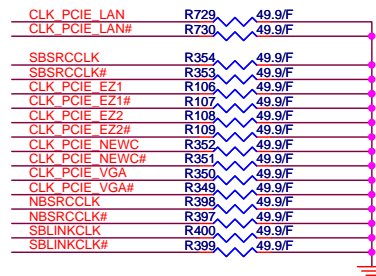


Speed table

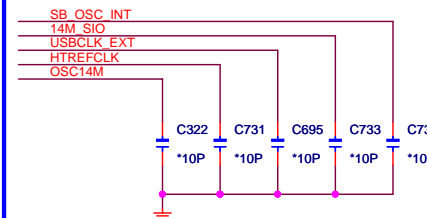
EXT CLK FREQUENCY SELECT TABLE(MHZ)

FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal HAMMER operation

Clock circuit



EMI



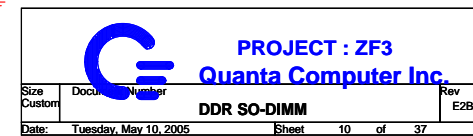
PROJECT : ZF3
Quanta Computer Inc.

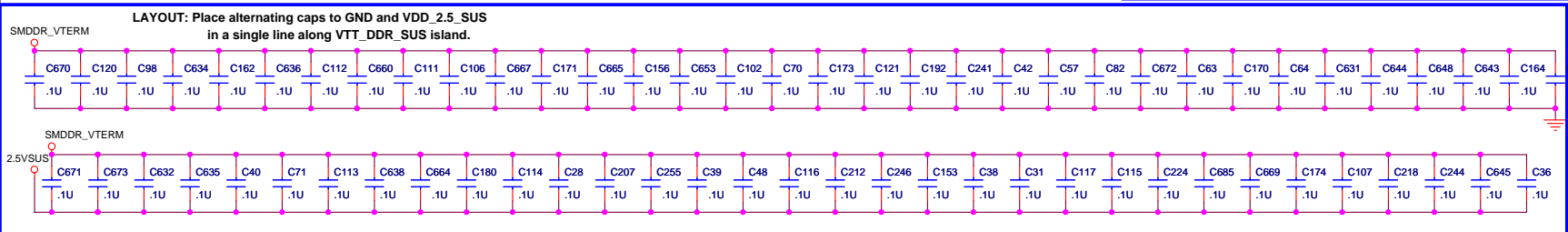
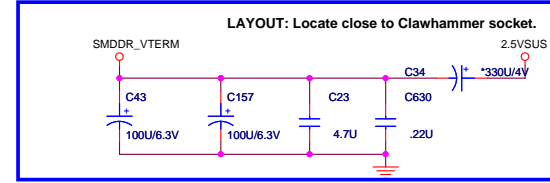
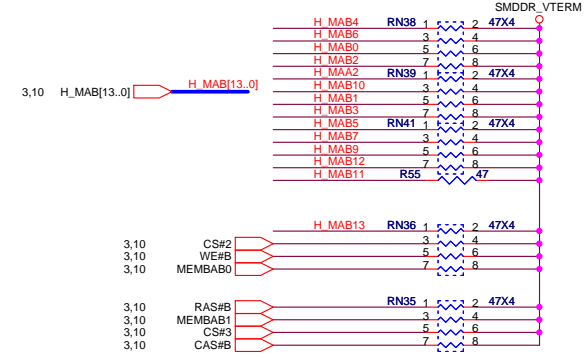
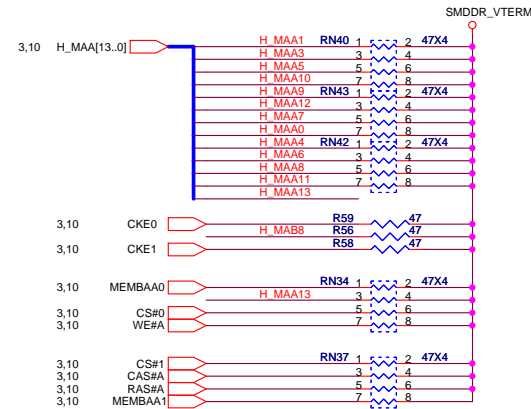
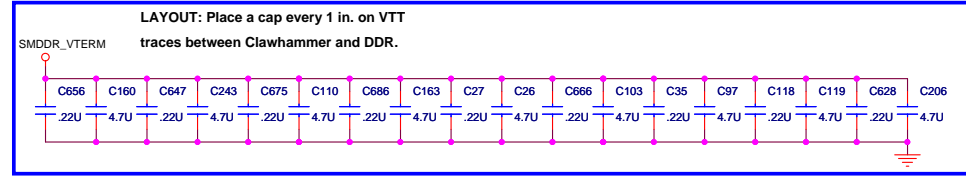
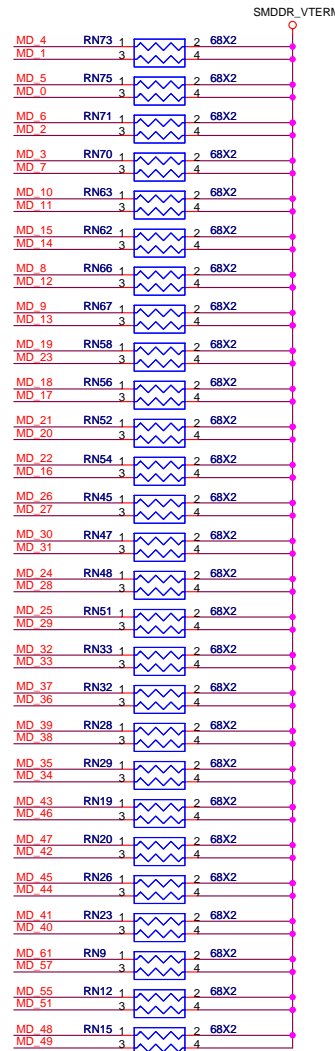
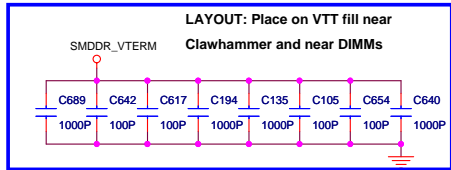
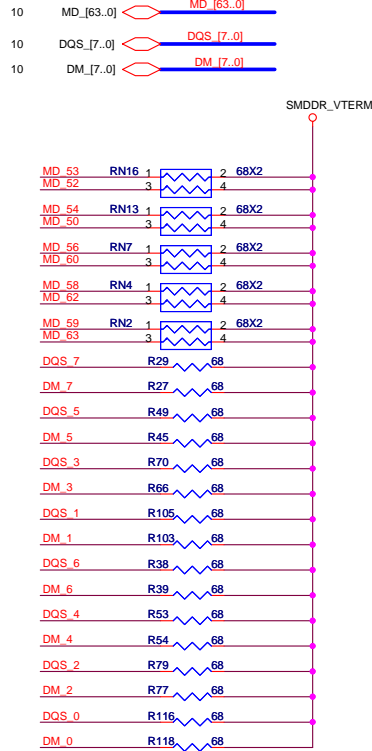
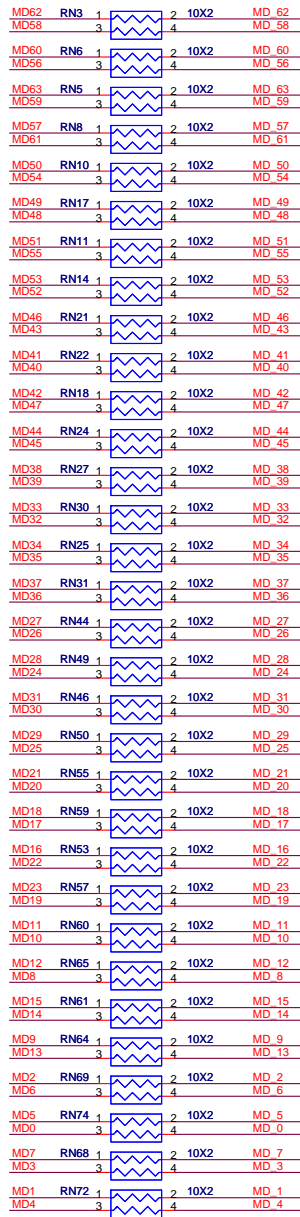
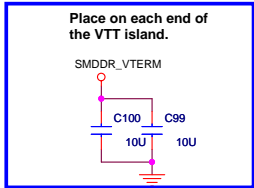
Size Custom	Document Number EXT CLOCK GENERATOR	Rev E2B
Date: Wednesday, May 11, 2005	Sheet 9 of 37	

Date: Wednesday, May 11, 2005 Sheet 9 of 37

Date: Wednesday, May 11, 2005 Sheet 9 of 37

WWW.AliSaler.Com

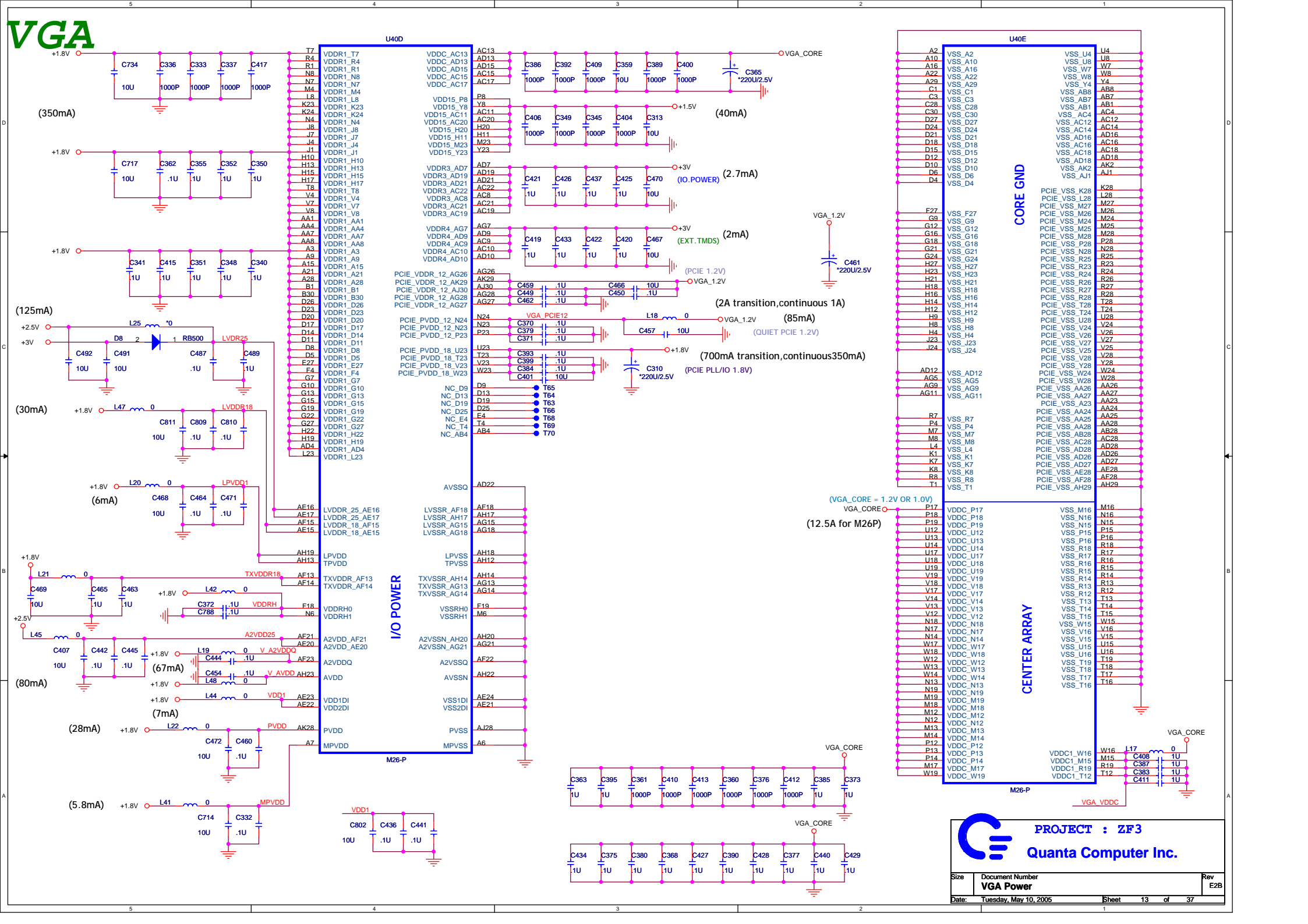


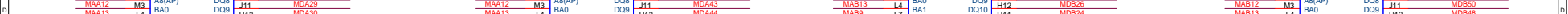




VGA RED	R447	150/F
VGA GRN	R446	150/F
VGA BLU	R445	150/F

Size	Document Number VGA Host	Rev E2
Date:	Tuesday, June 07, 2005	Sheet 12 of 37





A

14 M. CLKAP R381 56

14 M. CLKAP R403 56

A



A

14 M. CLKAP R381 56

14 M. CLKAP R403 56

A



A

14 M. CLKAP R381 56

14 M. CLKAP R403 56

A

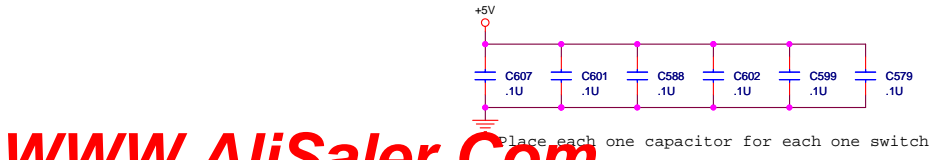
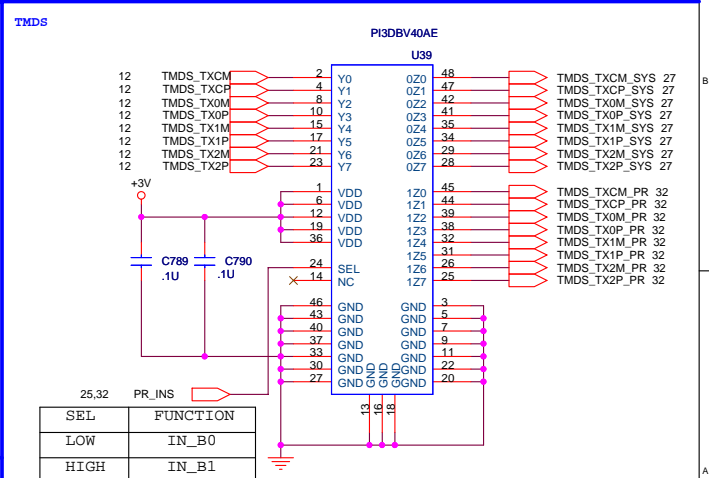
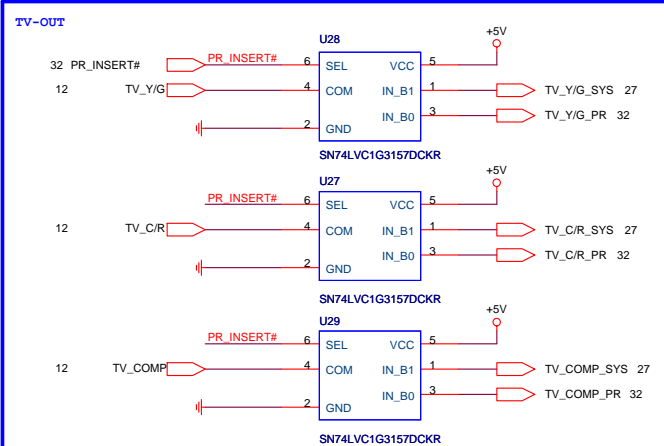
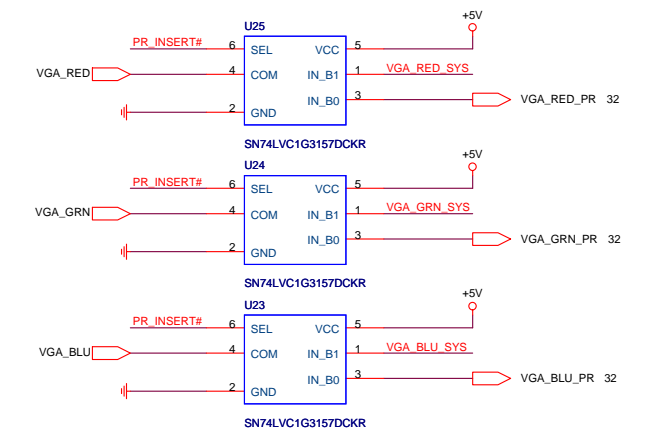
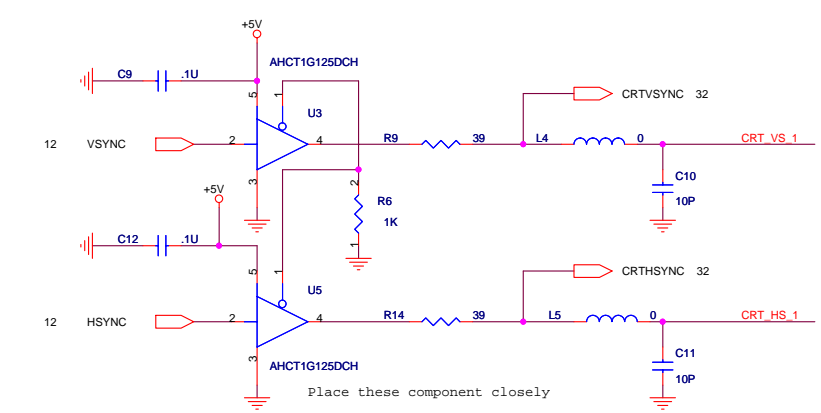
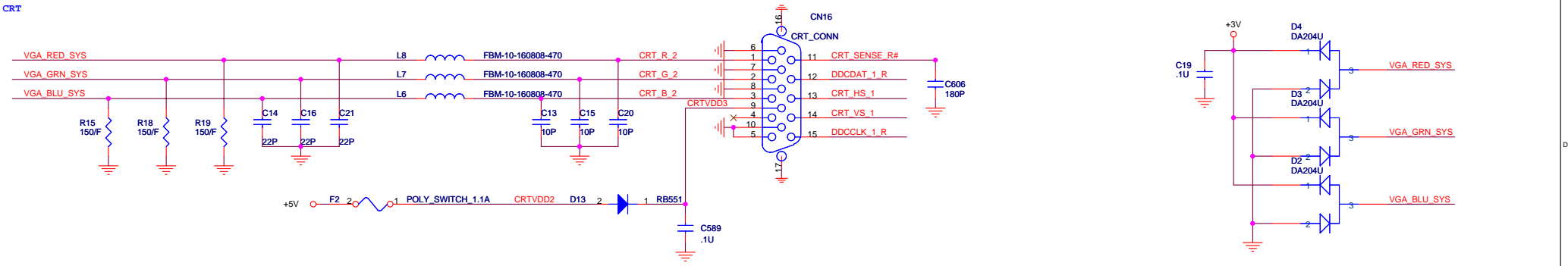


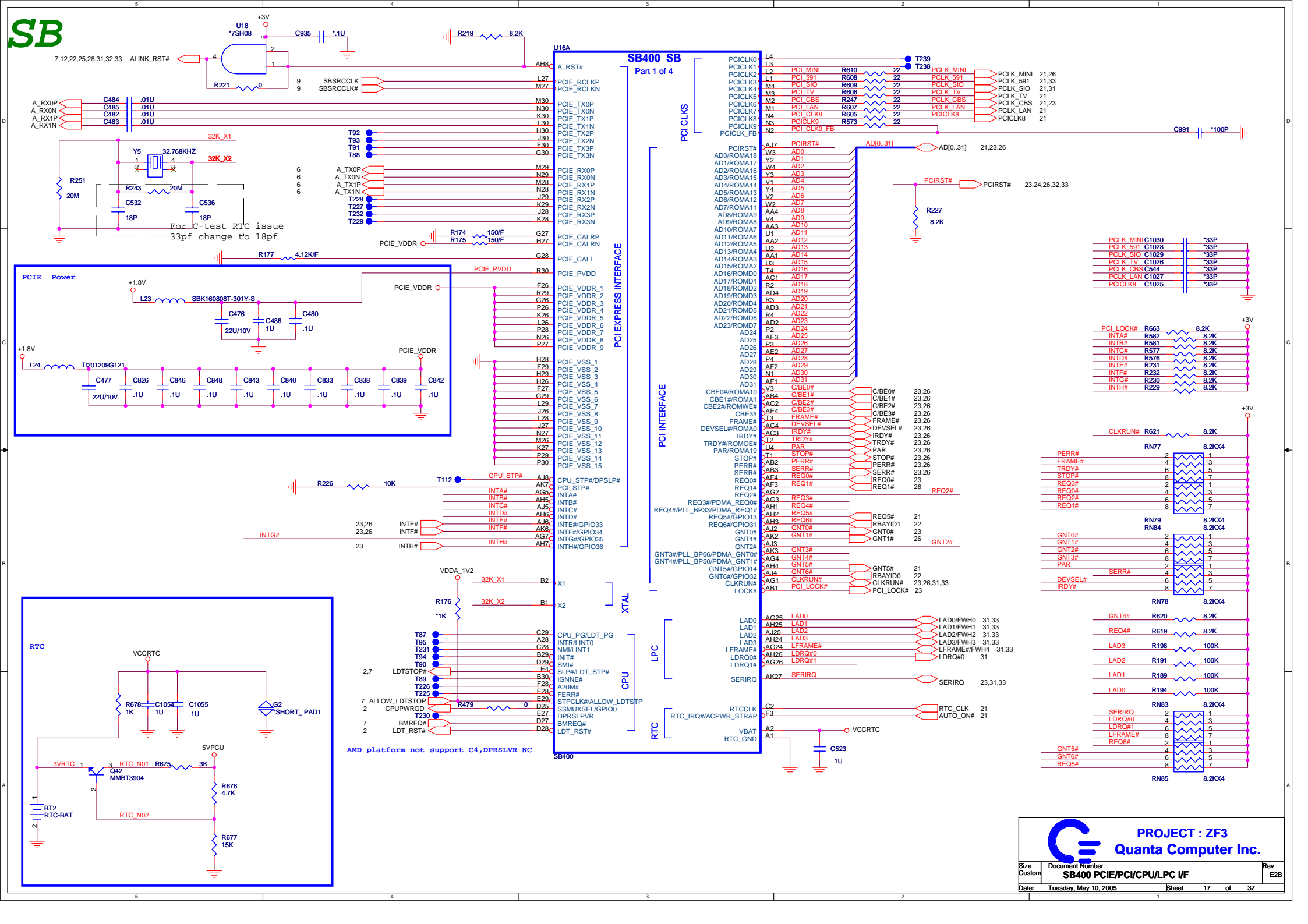
A

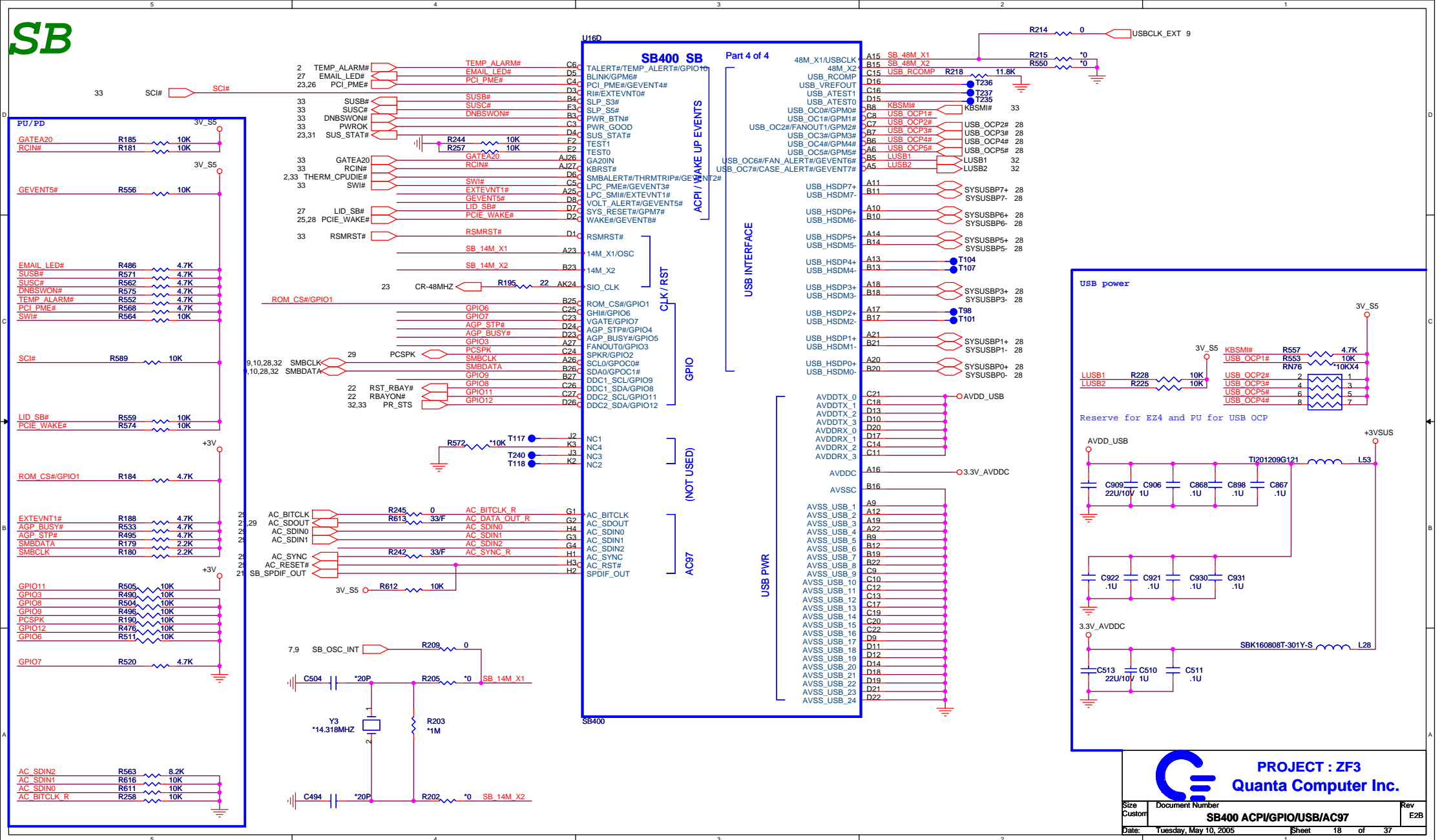
14 M. CLKAP R381 56

14 M. CLKAP R403 56

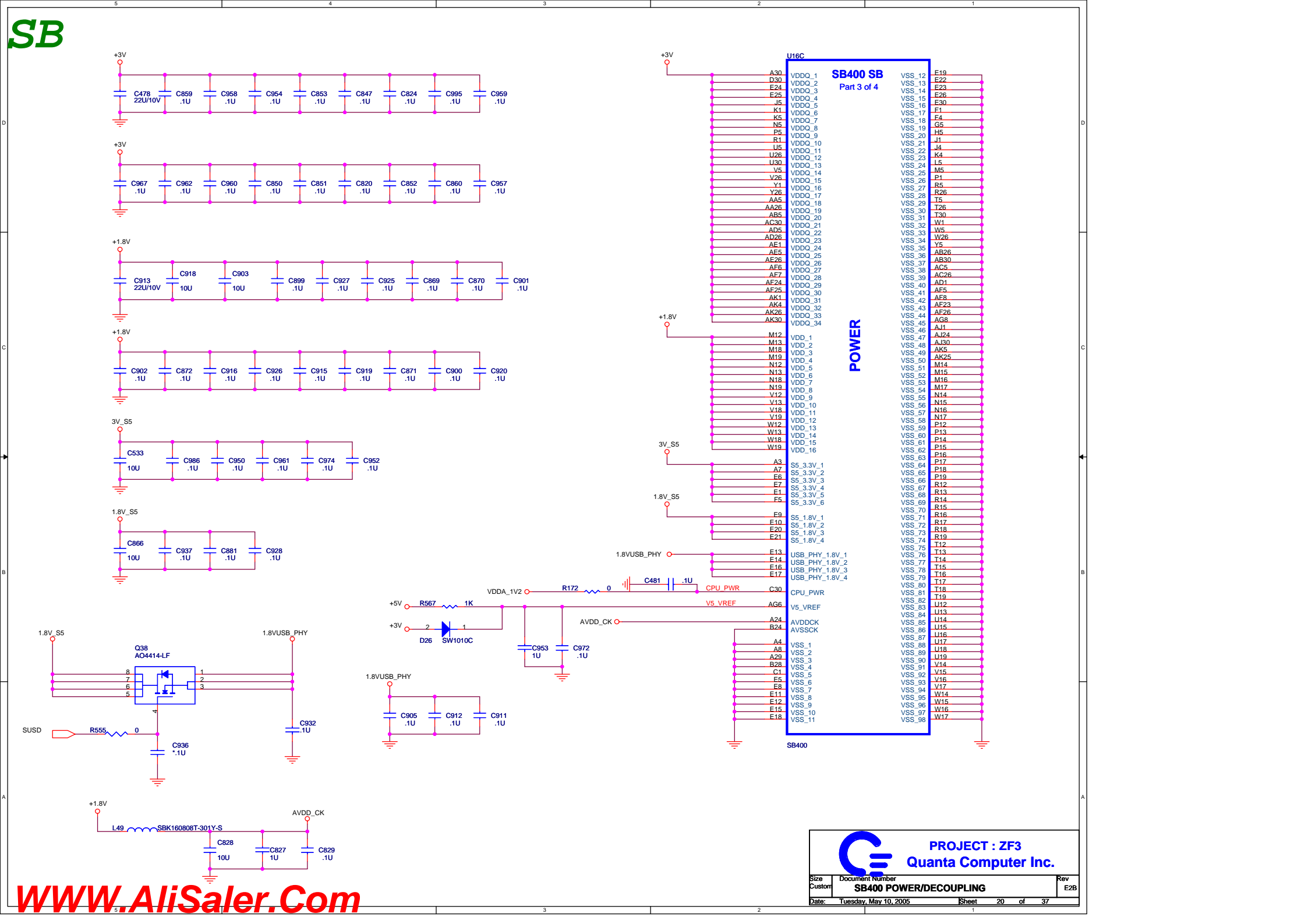
A

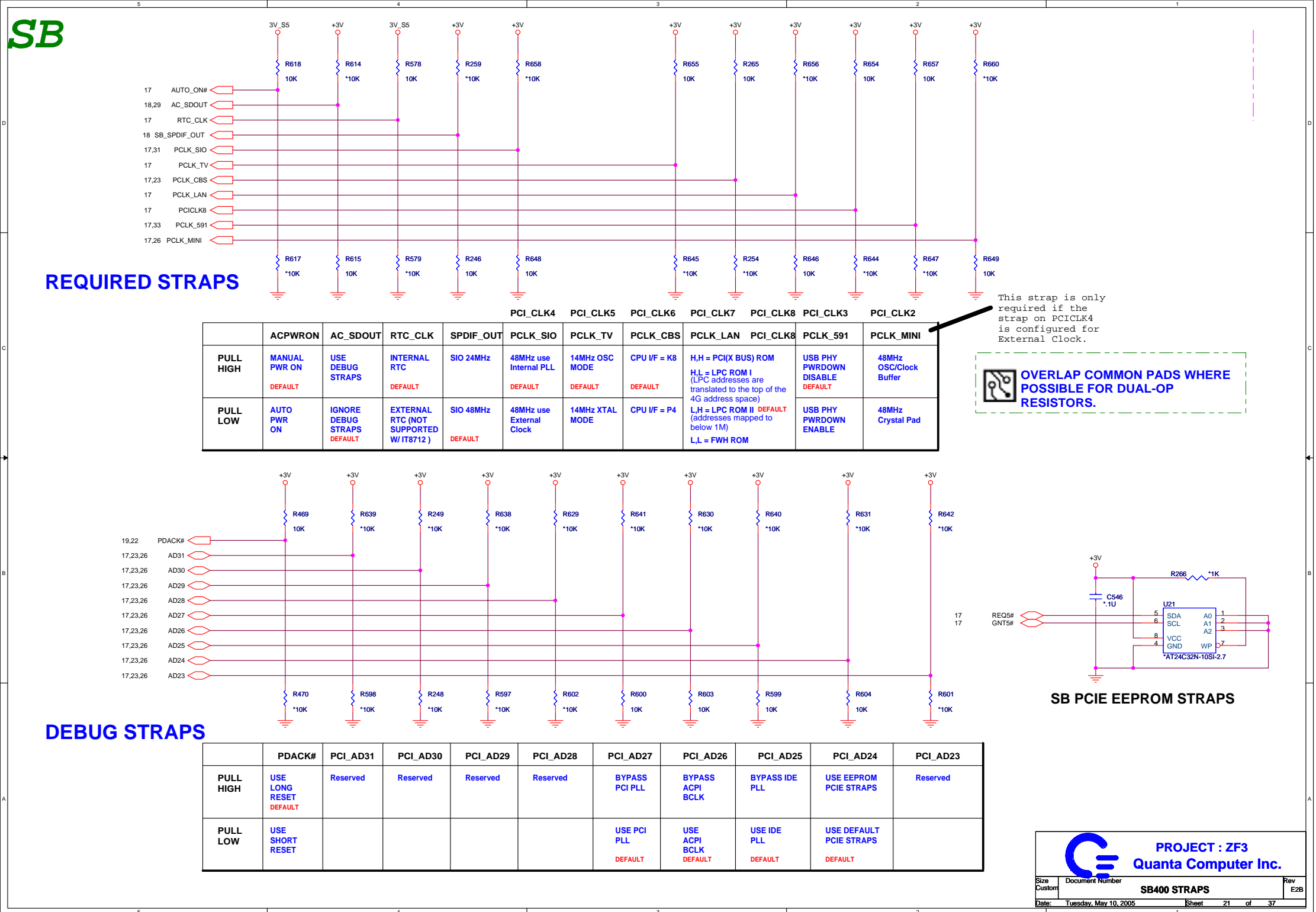










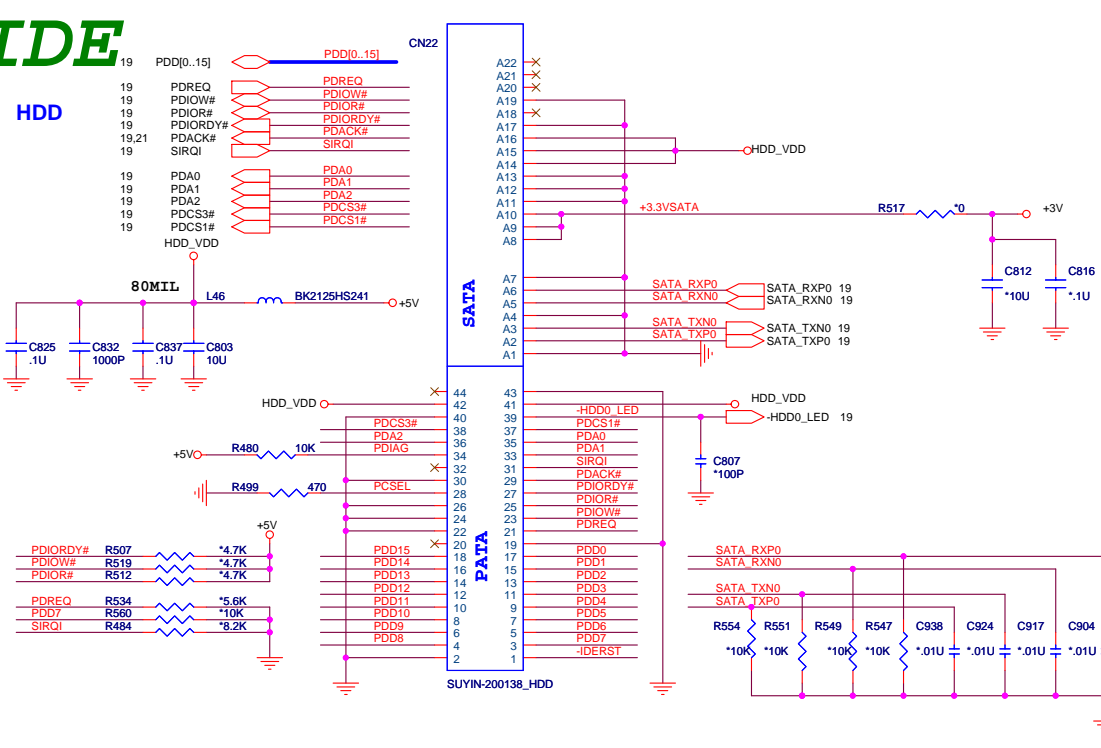


This strap is only required if the strap on PCI_CLK4 is configured for External Clock.

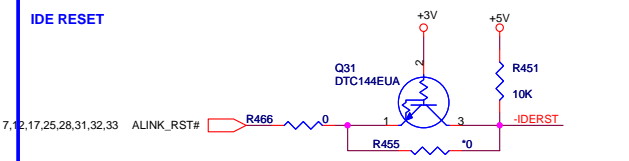
 **OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.**

IDE

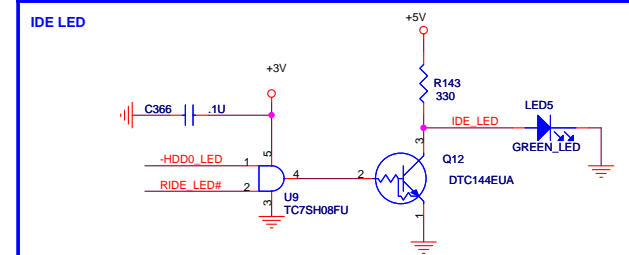
HDD



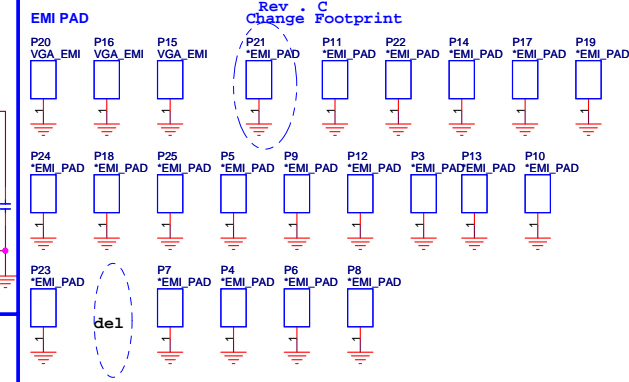
IDE RESET



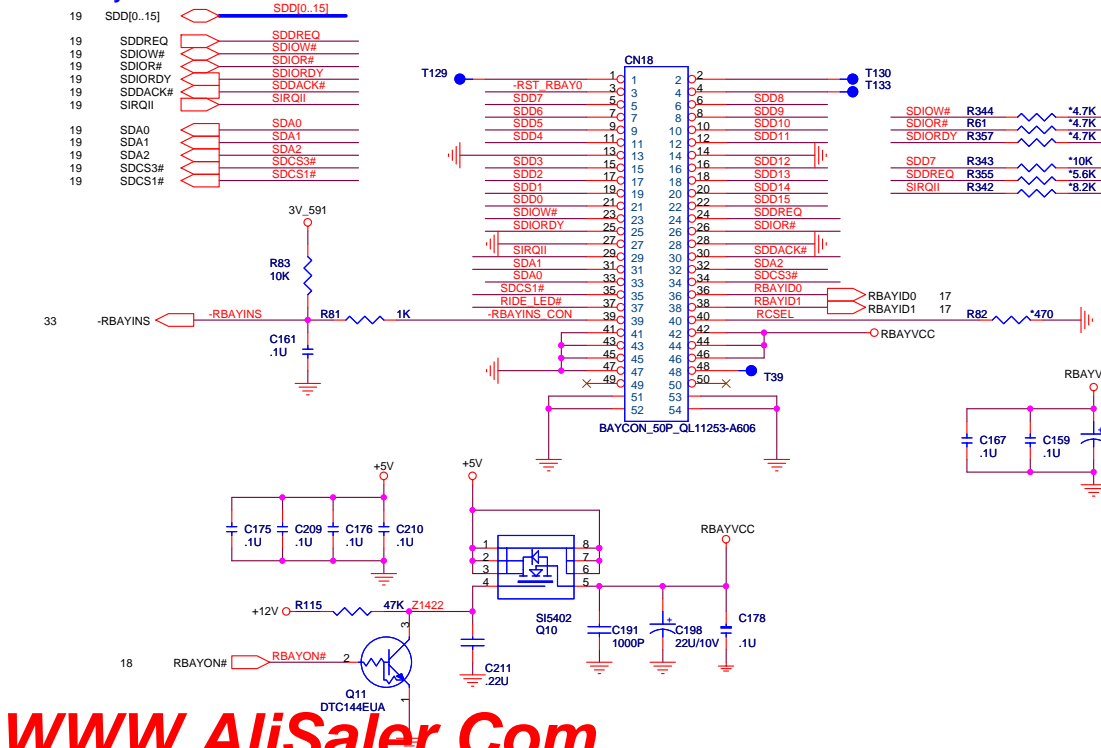
IDE LED



EMI PAD

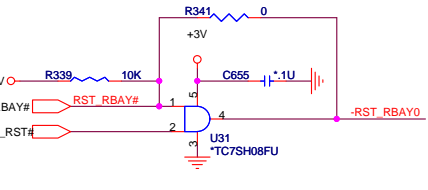


Multi-Bay Connector



BAY ID STATUS

RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	1	HDD
1	0	CD/DVD



WWW.AliSaler.Com

PROJECT : ZF3
Quanta Computer Inc.

Size Custom

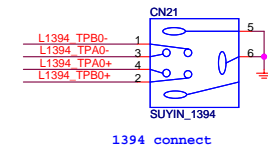
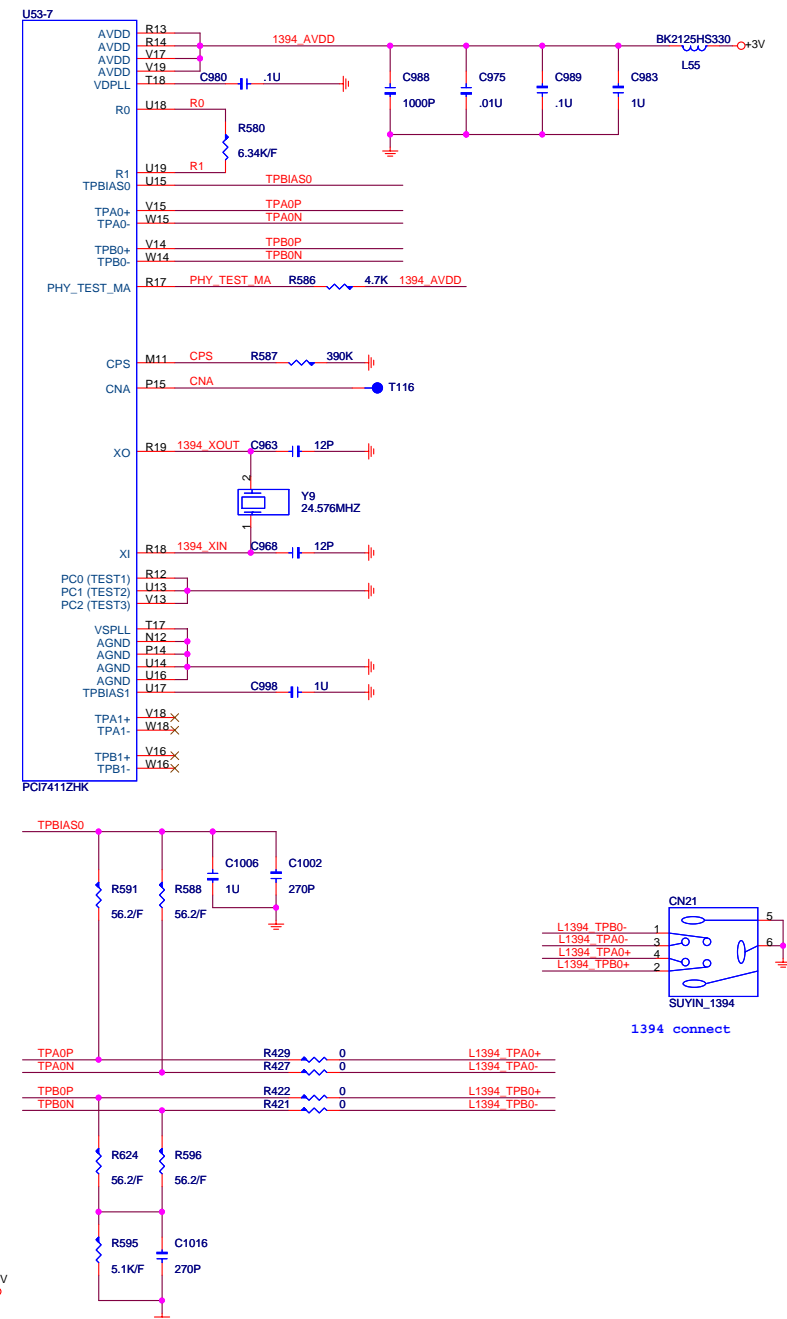
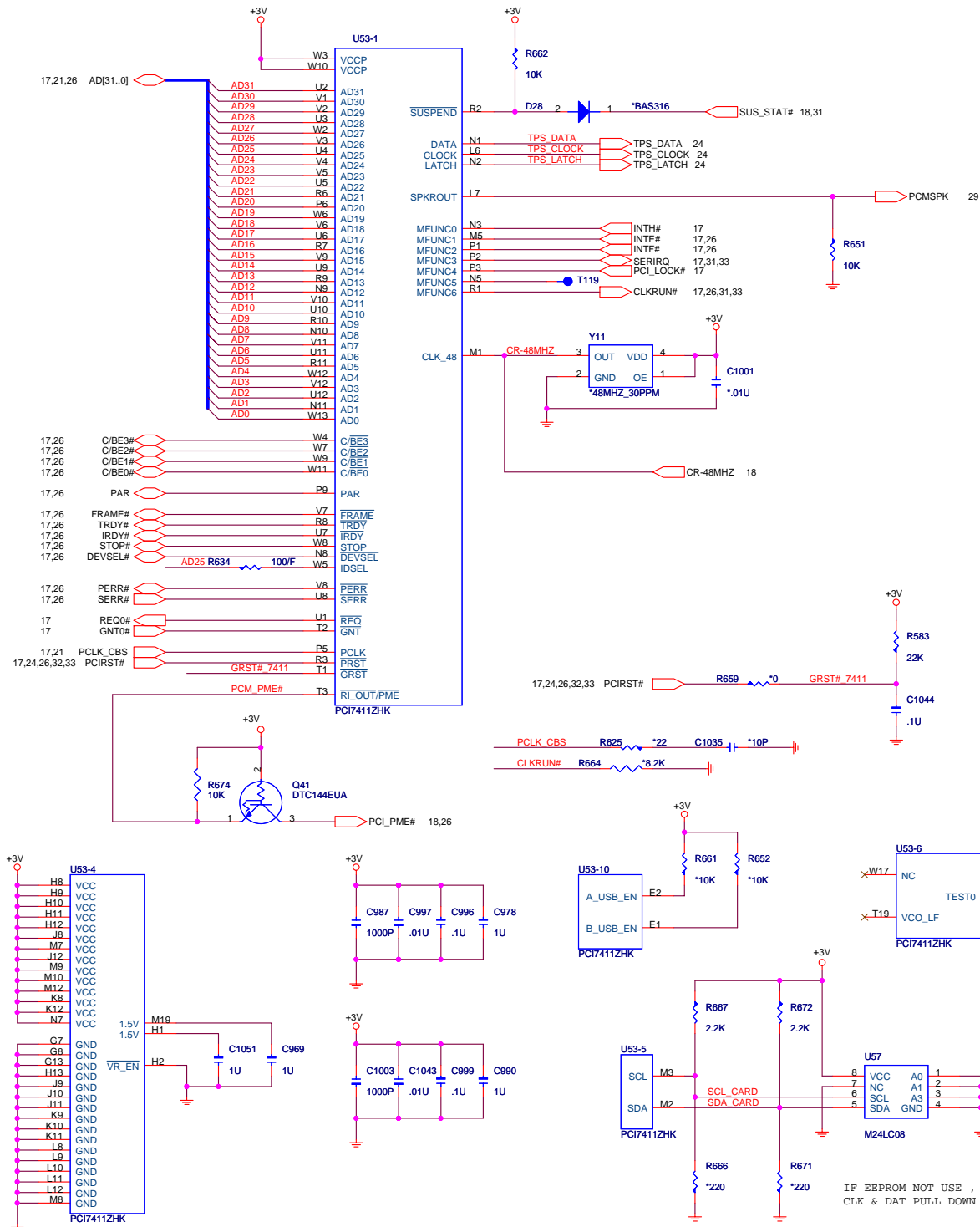
Document Number
HDD/CDROM/EMI PAD

Date: Wednesday, May 11, 2005

Rev
E2B

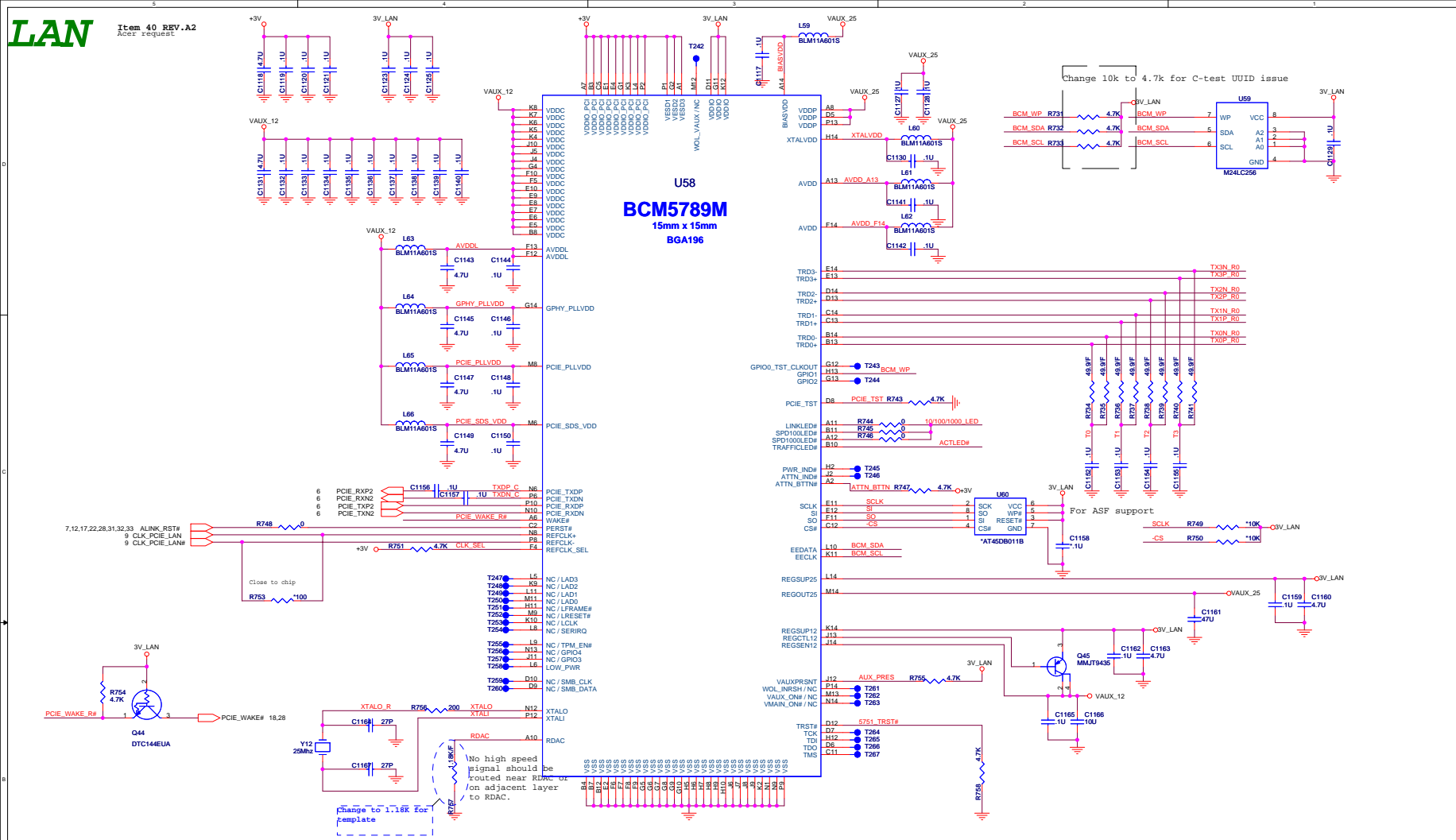
Sheet 22 of 37

7411

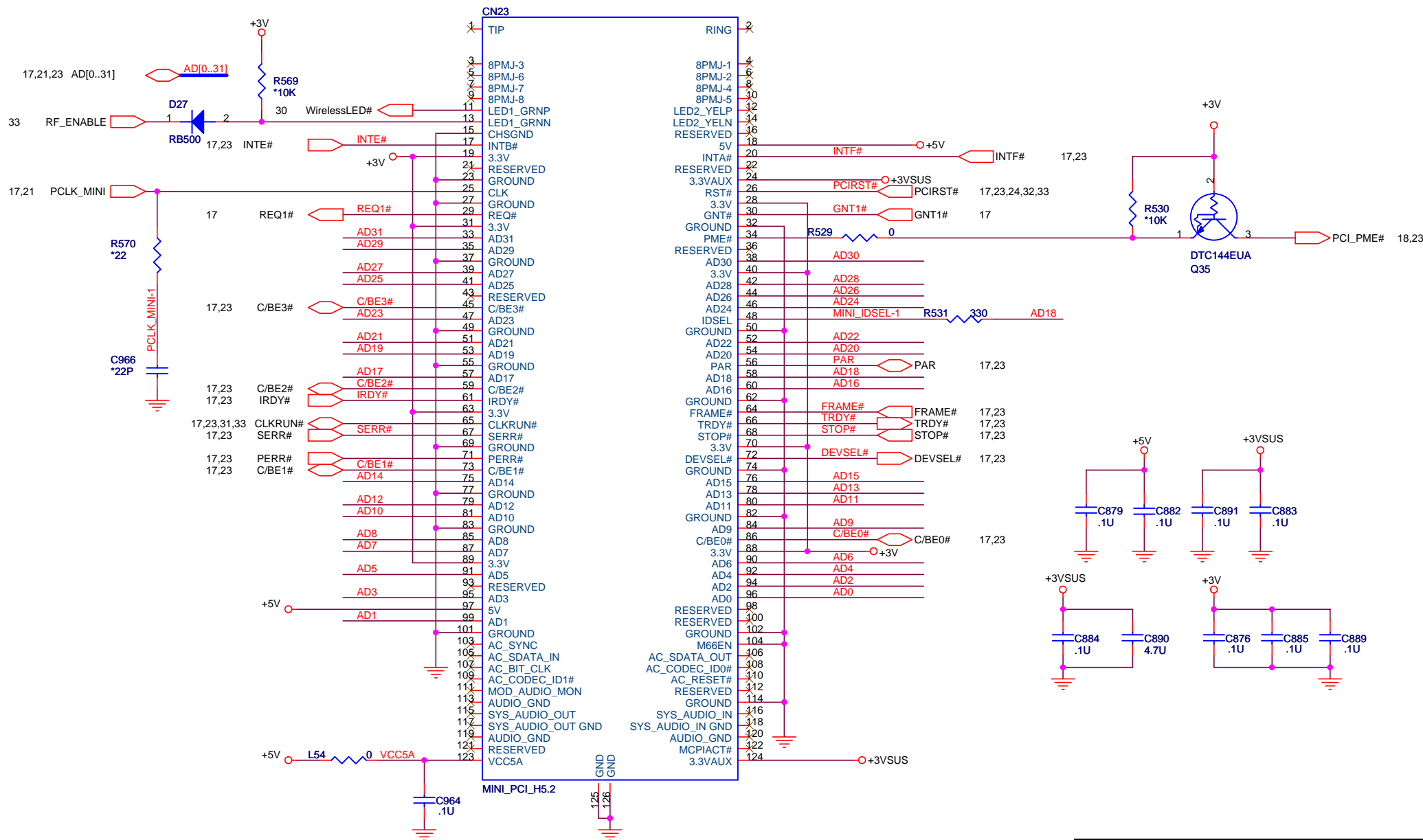


7411

4 IN1 CARD READER
XD,MMC/SD,MS/MSP



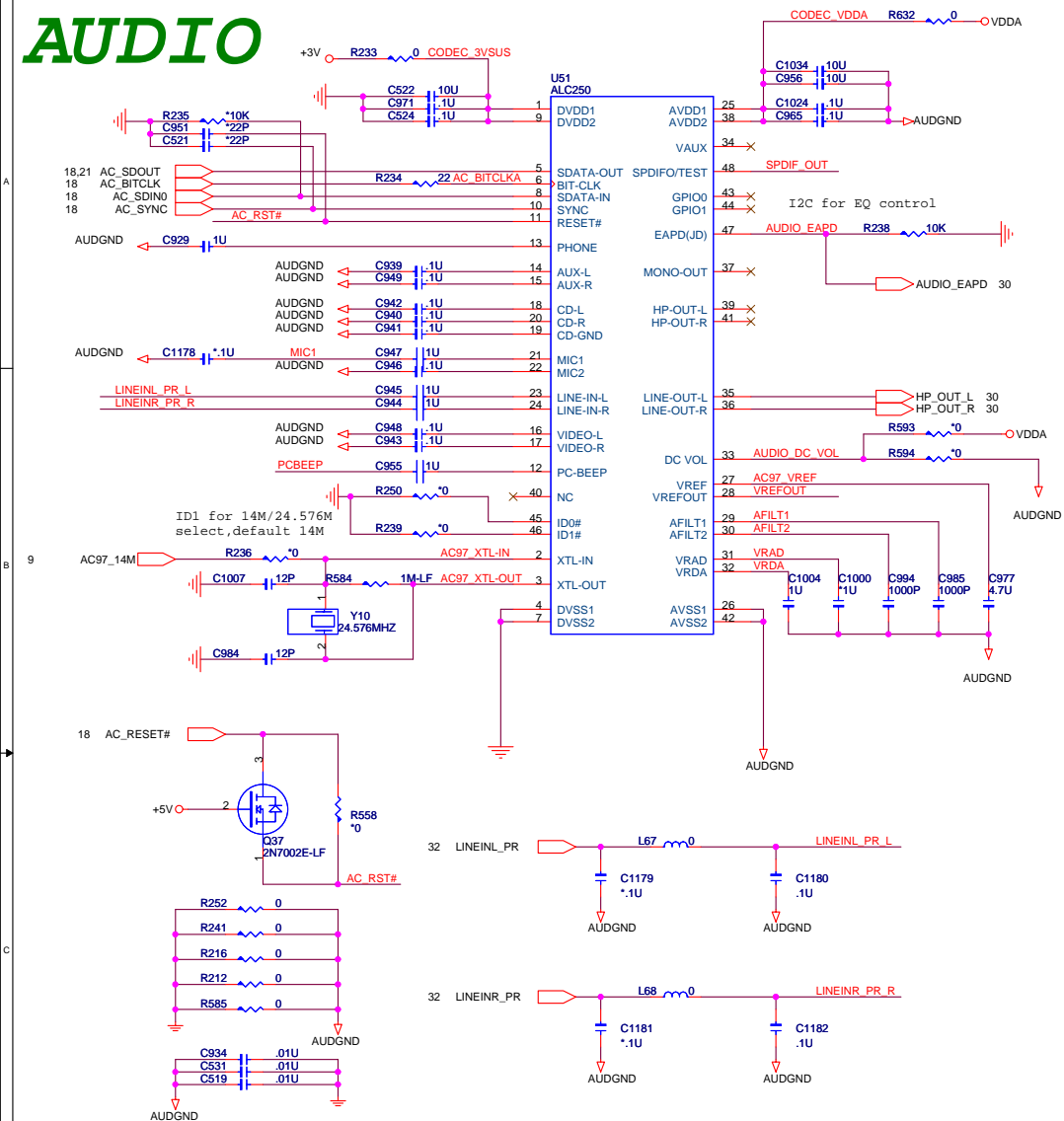
MINI PCI



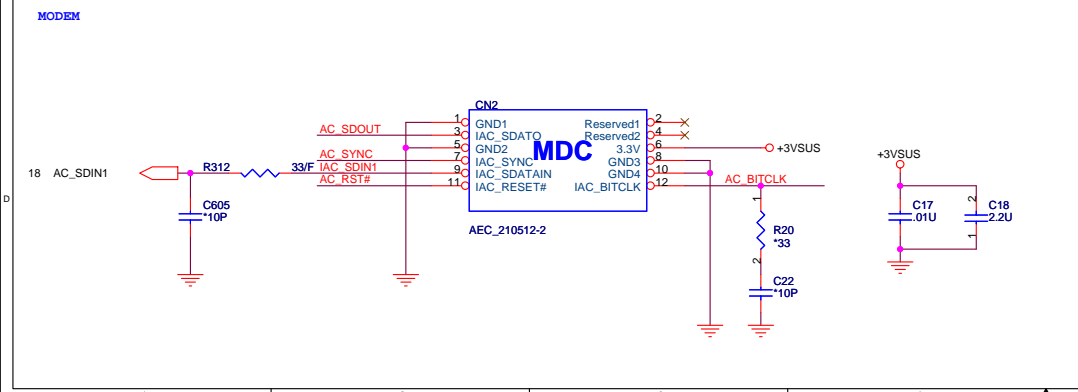
PROJECT : ZF3
Quanta Computer Inc.

Size Custom	Document Number MININ PCI	Rev E2B
Date: Wednesday, May 11, 2005	Sheet 26 of 37	

AUDIO



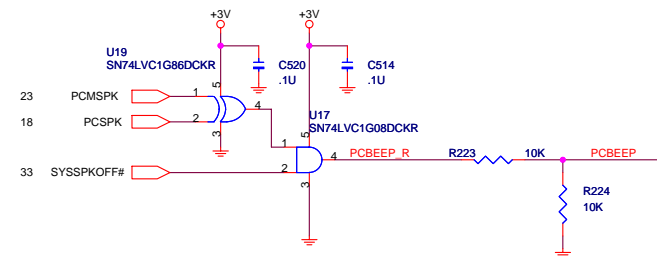
MODEM



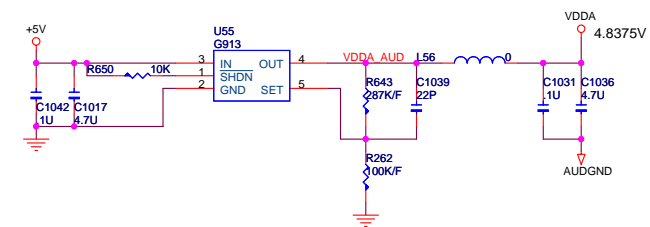
SPDIF



BEEP

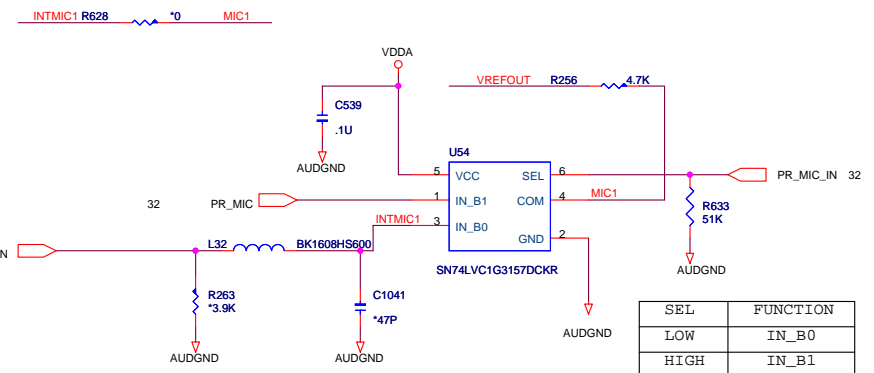


CODEC POWER



MIC

If no docking use this path



SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



PROJECT : ZF3
Quanta Computer Inc.

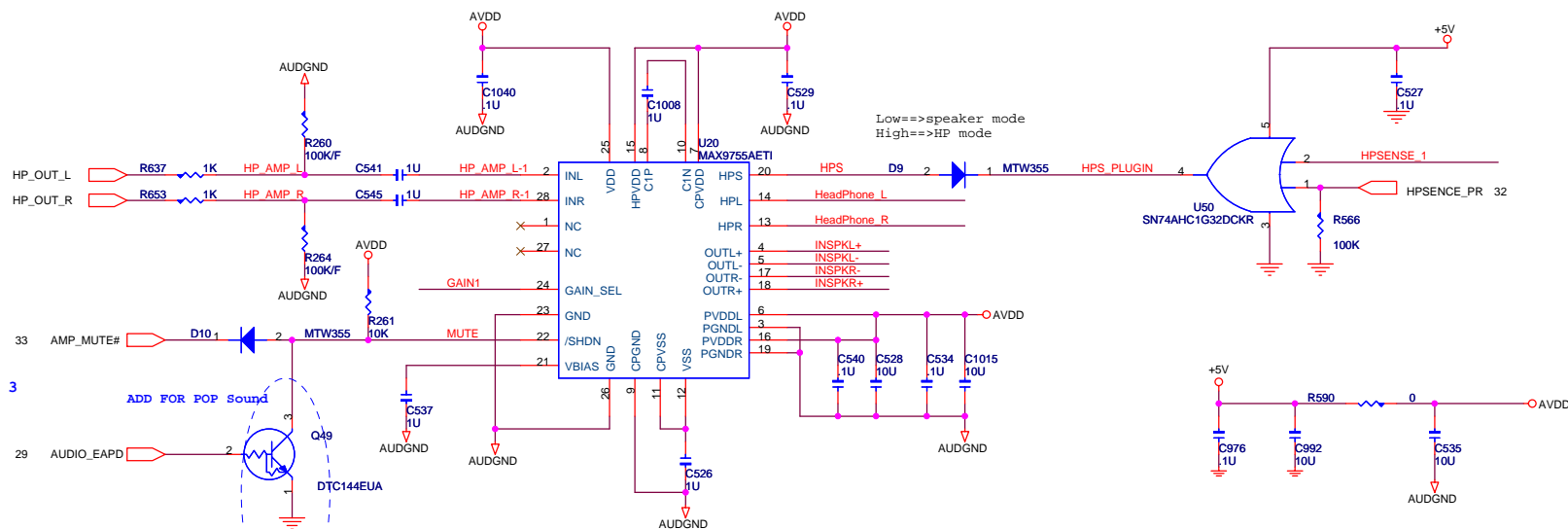
Size Custom	Document Number CODEC	Rev E2B
Date:	Wednesday, May 11, 2005	Sheet 29 of 37

AUDIO

AMP

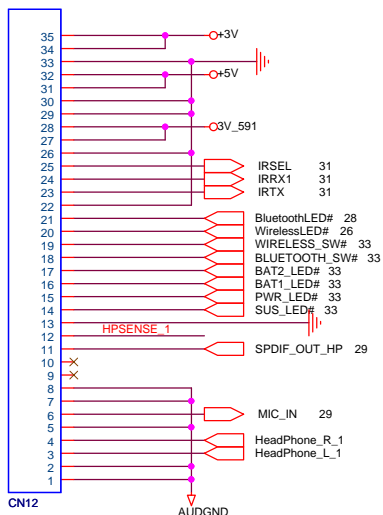
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0

Item25 Change Gain to 10.5 , 3

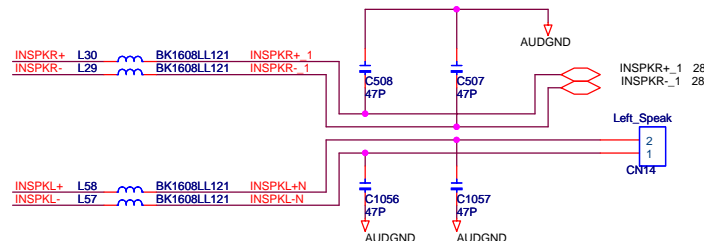


Audio conn

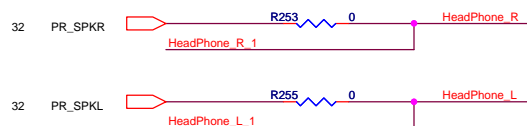
ACES_88060_3500



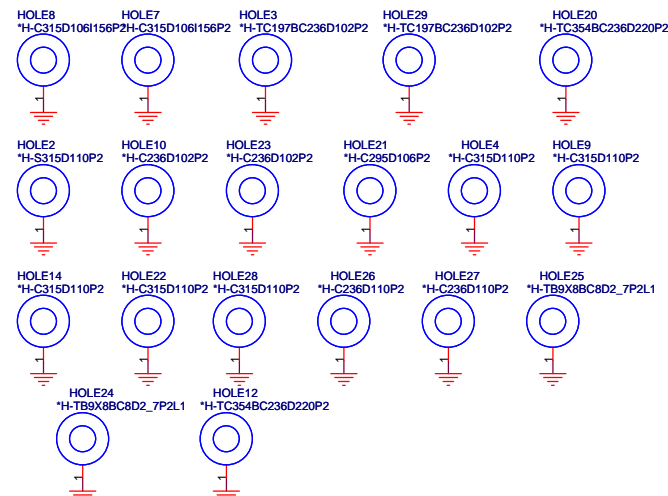
Internal speaker



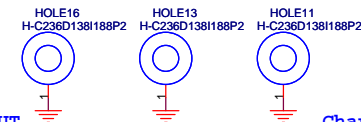
Headphone



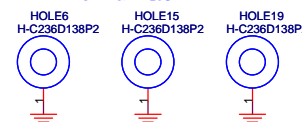
HOLE



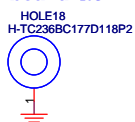
CPU NUT



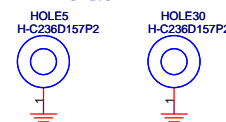
Thermal NUT



Charger board NUT



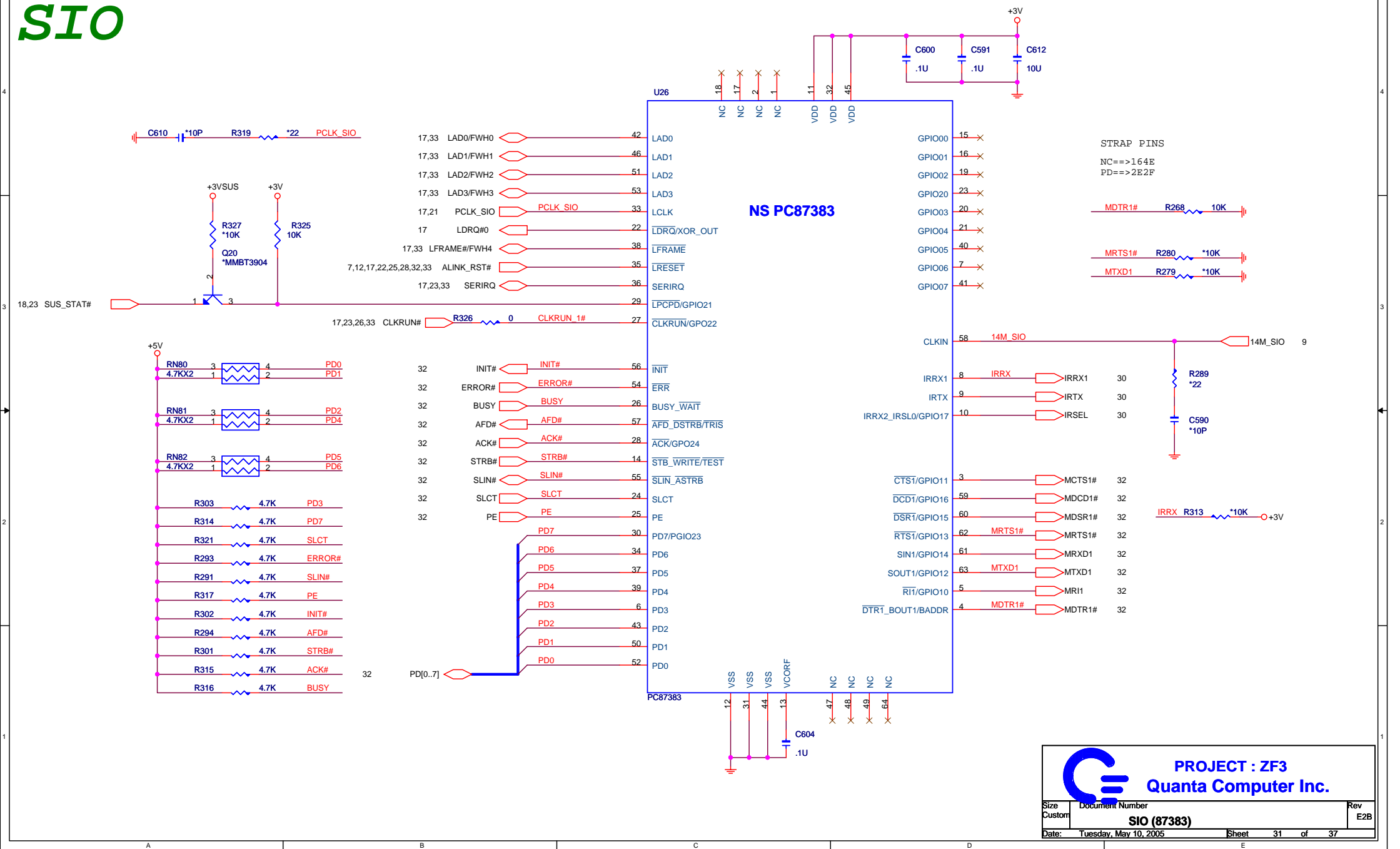
MDC NUT



PROJECT : ZF3
Quanta Computer Inc.

Size Custom	Document Number	Rev E2B
AUDIO AMP & JACK & HOLE		
Date: Tuesday, May 10, 2005	Sheet 30	of 37

SIO



EZ4

